

Fig. 1 PRIOR ART

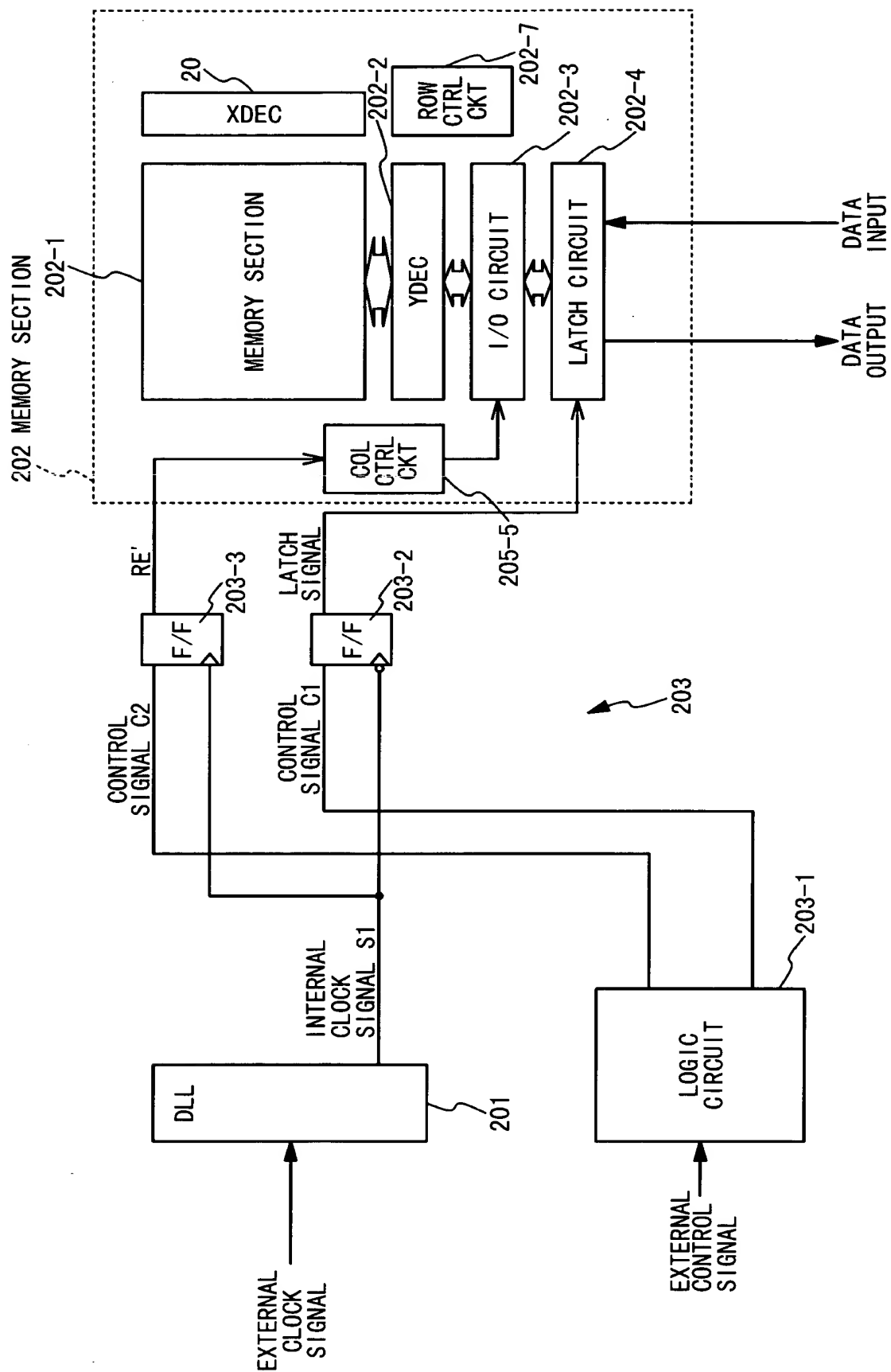


Fig. 2A
PRIOR ART



Fig. 2B
PRIOR ART



Fig. 2C
PRIOR ART



Fig. 2D
PRIOR ART



Fig. 2E
PRIOR ART

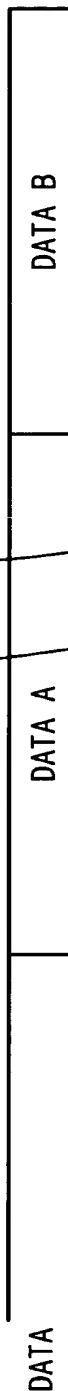


Fig. 2F
PRIOR ART



Fig. 2G
PRIOR ART



LATCH DATA

Fig. 3 PRIOR ART

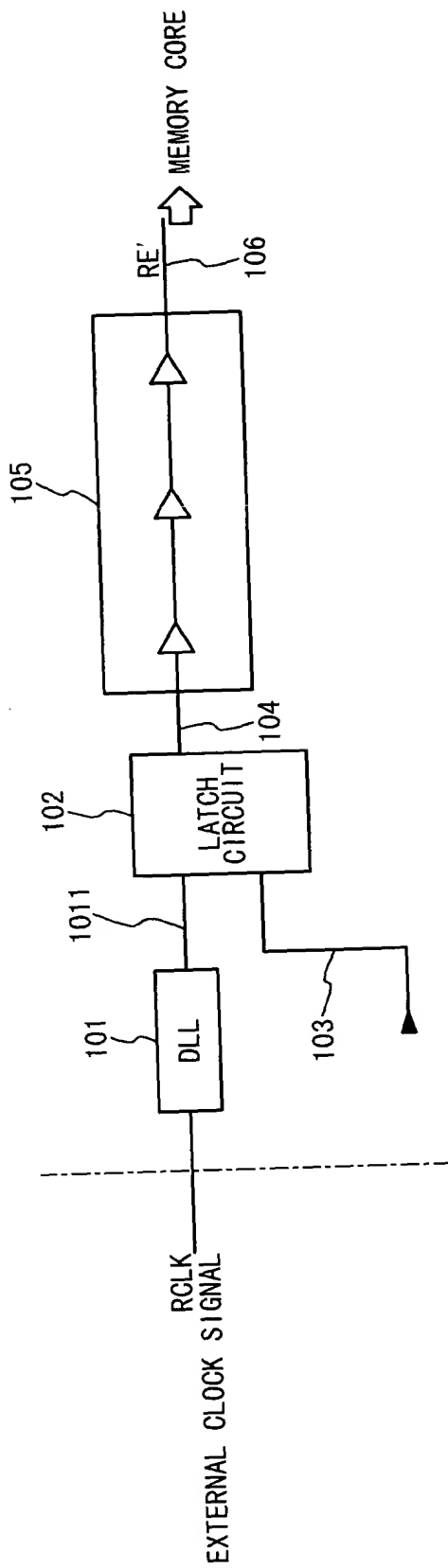


Fig. 4A

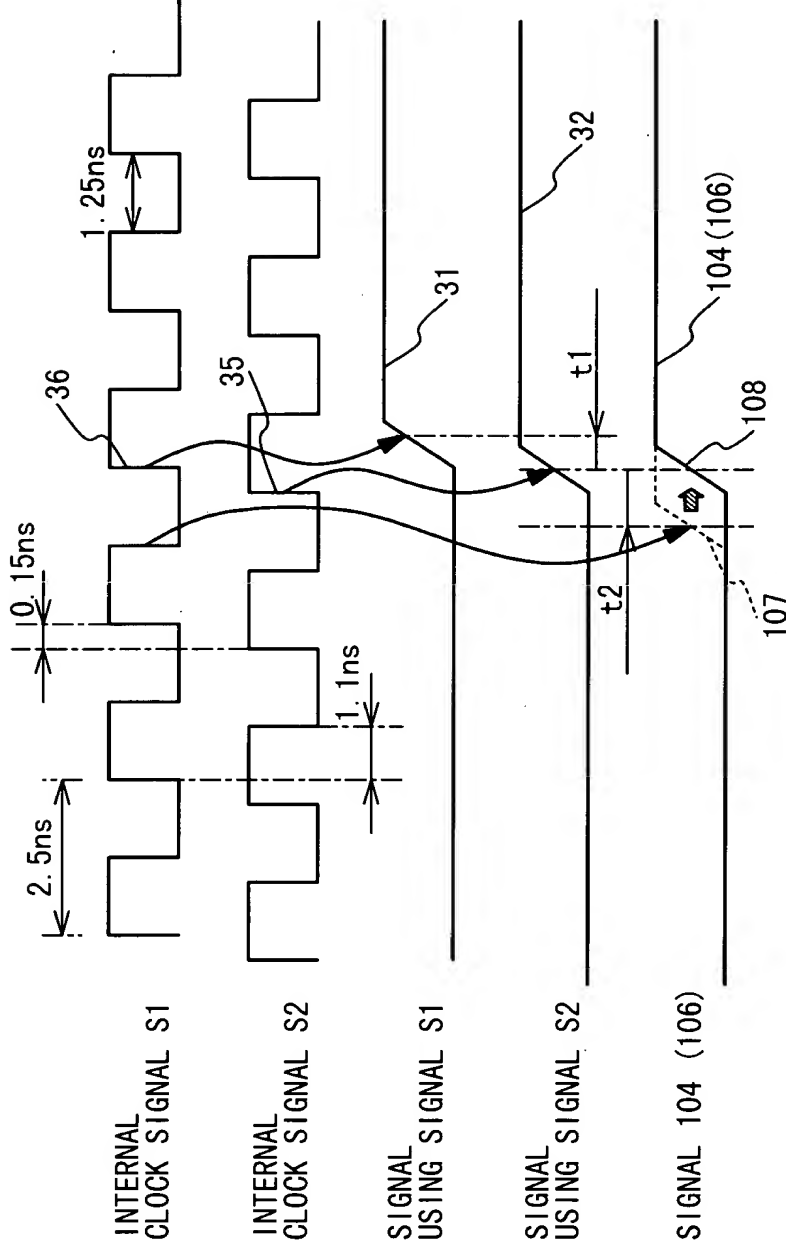


Fig. 4B

Fig. 4C

PRIOR ART

Fig. 4D

Fig. 4E

PRIOR ART

Fig. 5A

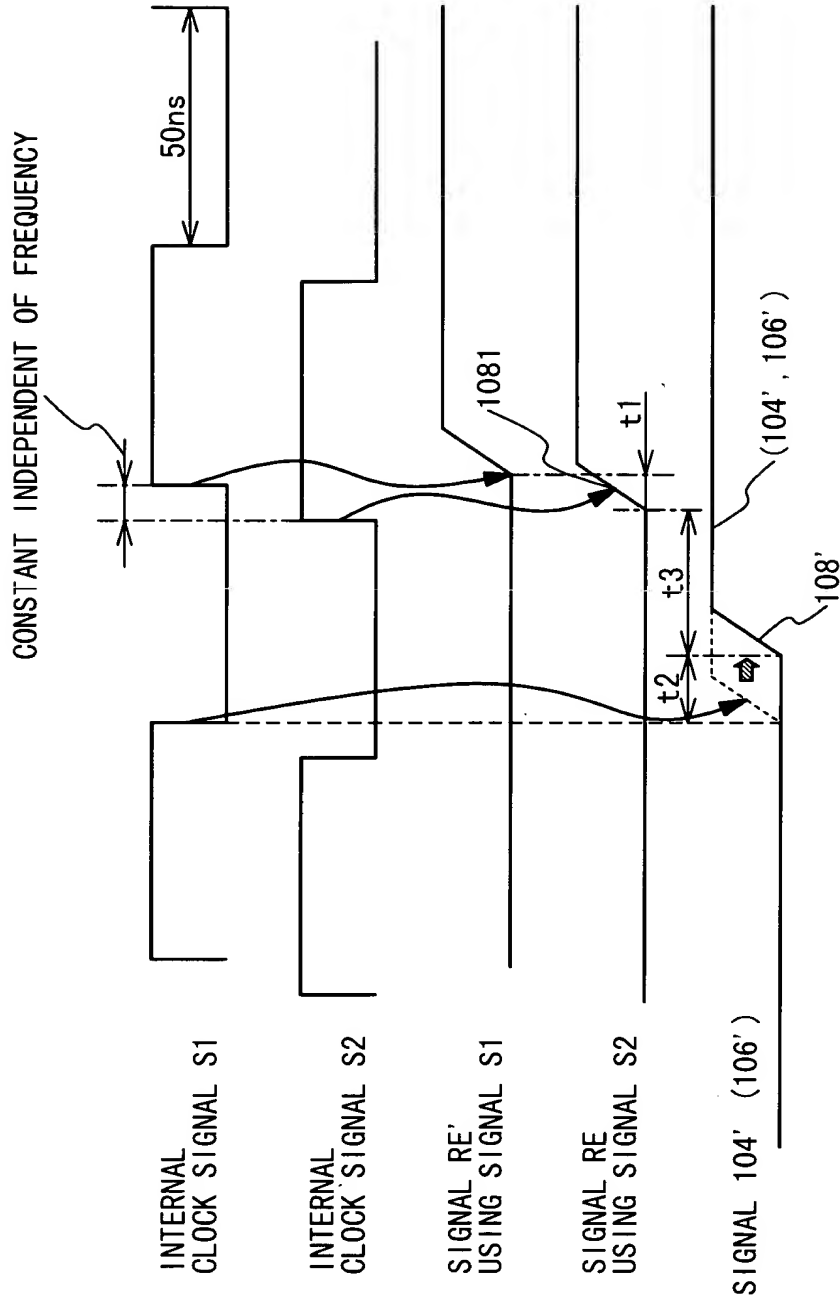


Fig. 5B

Fig. 5C

PRIOR ART

Fig. 5D

Fig. 5E

PRIOR ART

Fig. 6A

PRIOR ART

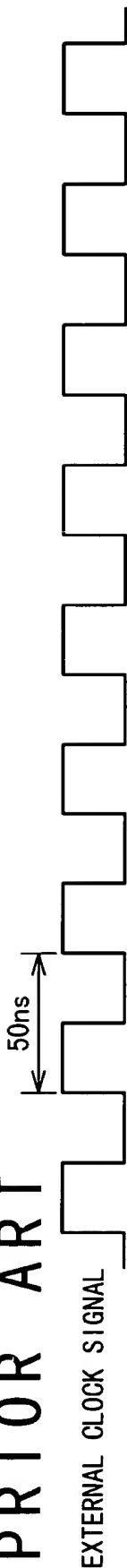


Fig. 6B

PRIOR ART

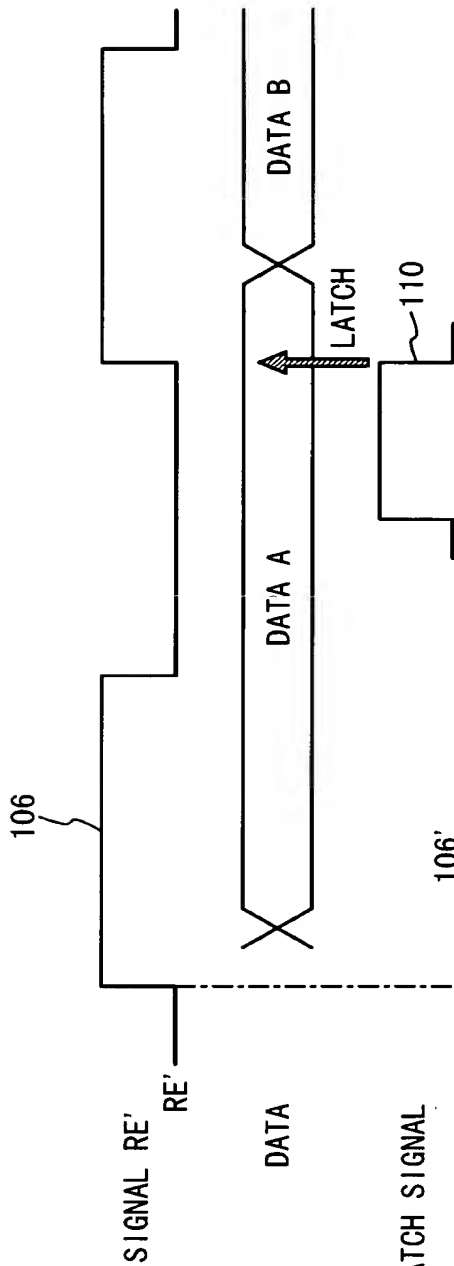


Fig. 6C

PRIOR ART

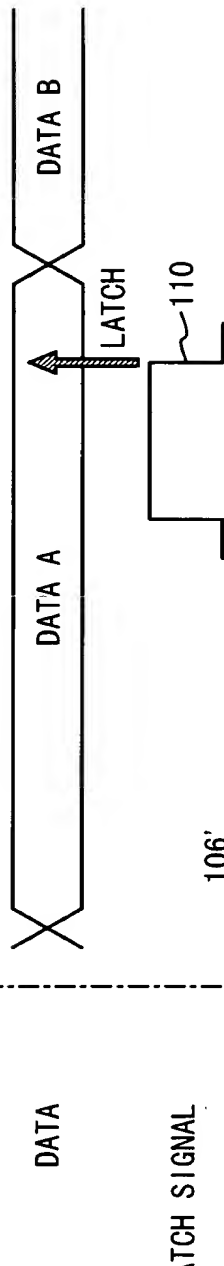


Fig. 6D

PRIOR ART



Fig. 6E

PRIOR ART

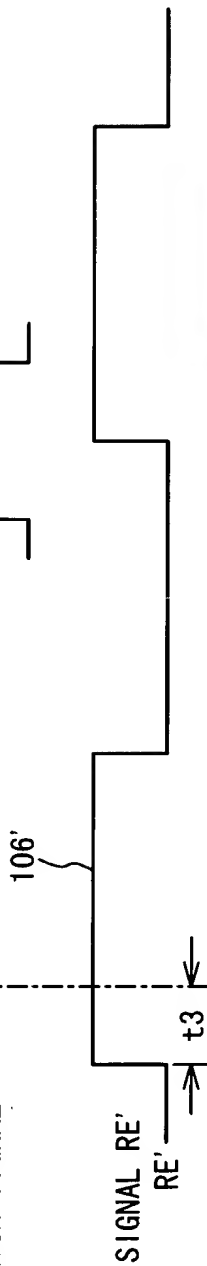


Fig. 6F

PRIOR ART

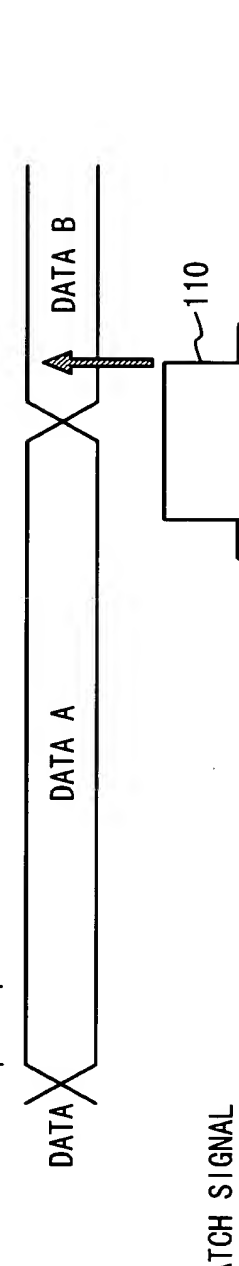


Fig. 6G

PRIOR ART



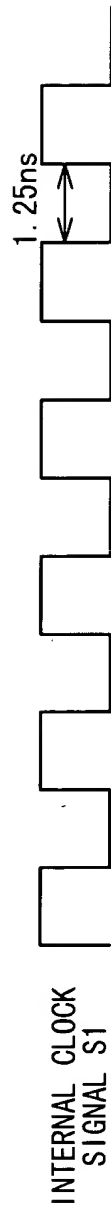


Fig. 7A



Fig. 7B

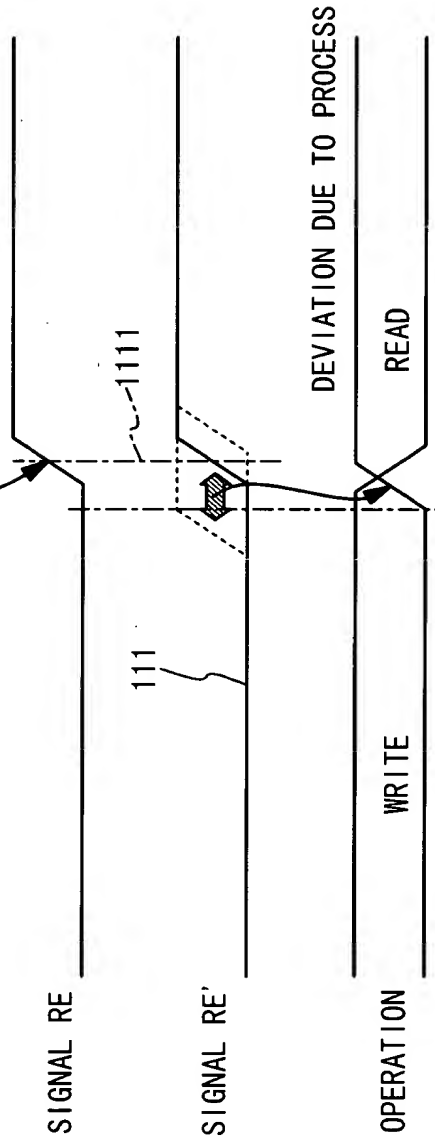


Fig. 7C

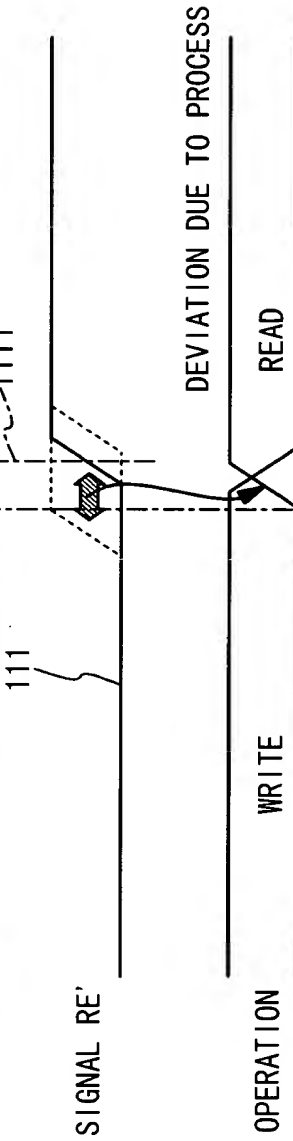


Fig. 7D

PRIOR ART

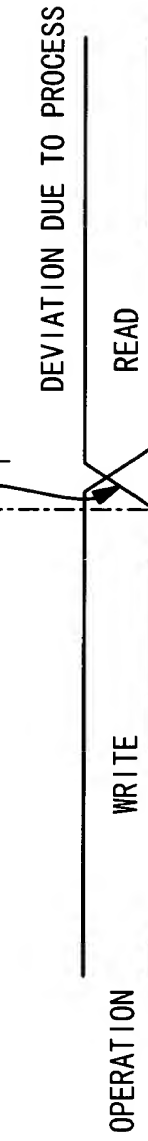


Fig. 7E

PRIOR ART

Fig. 8A

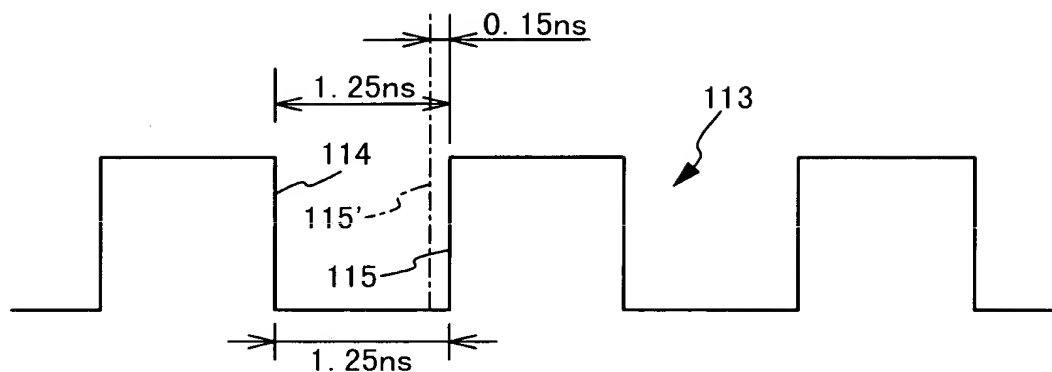


Fig. 8B PRIOR ART

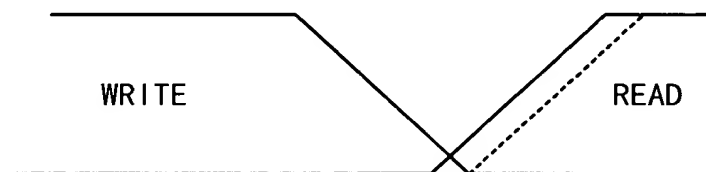
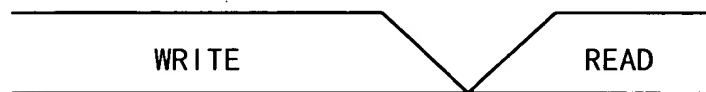


Fig. 8C PRIOR ART



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Fig. 9 PRIOR ART

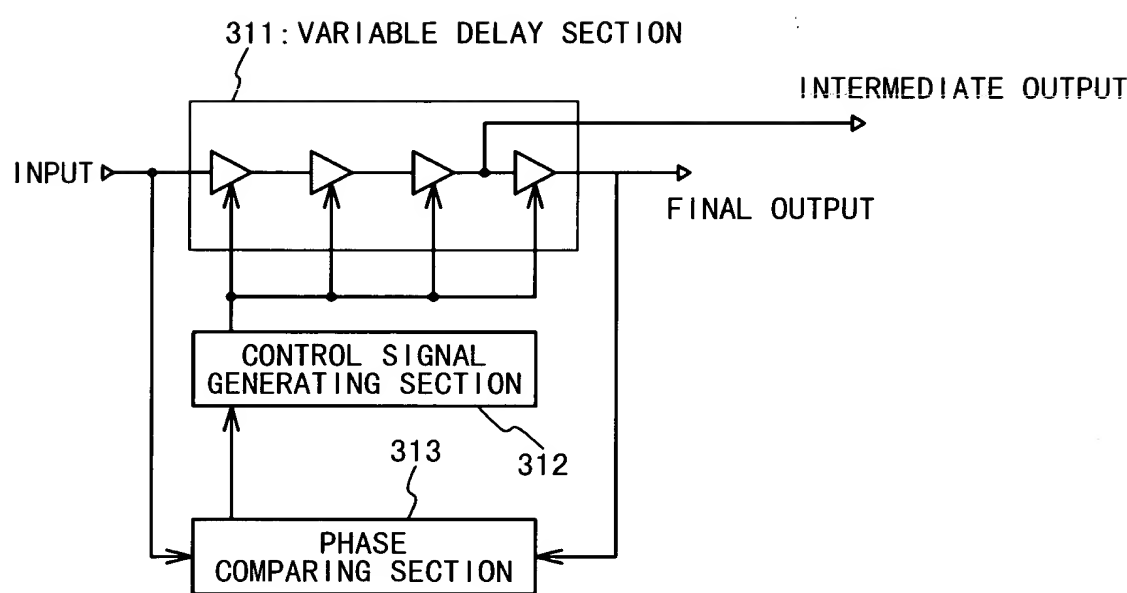


Fig. 10

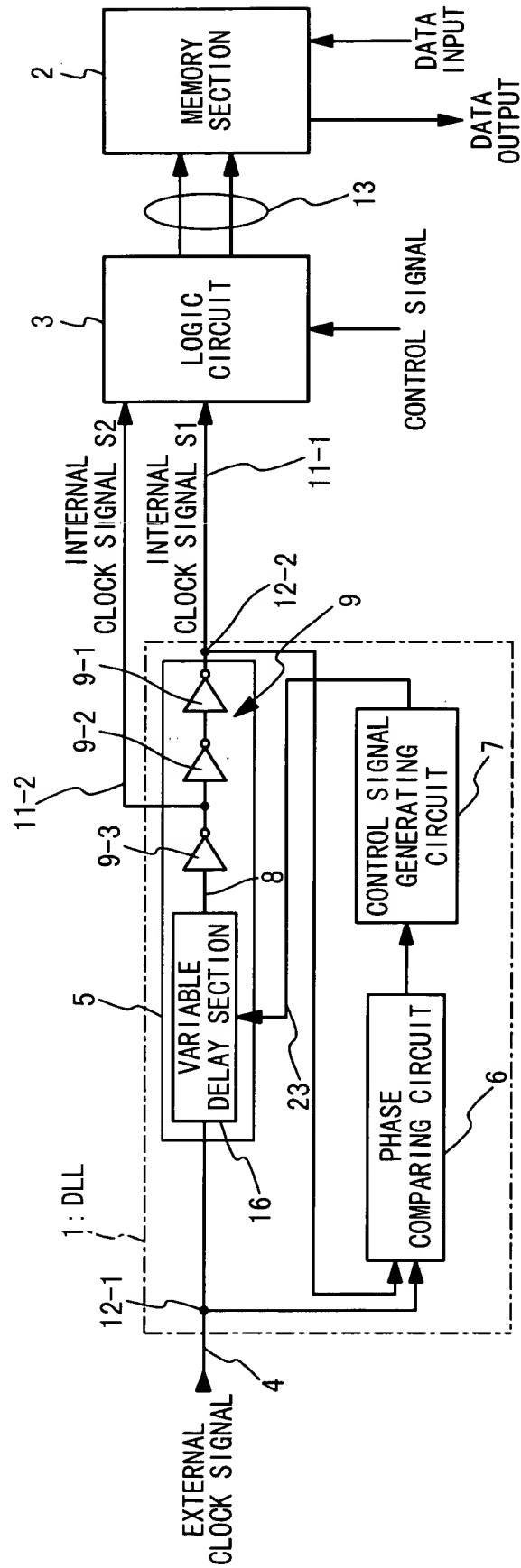


Fig. 11

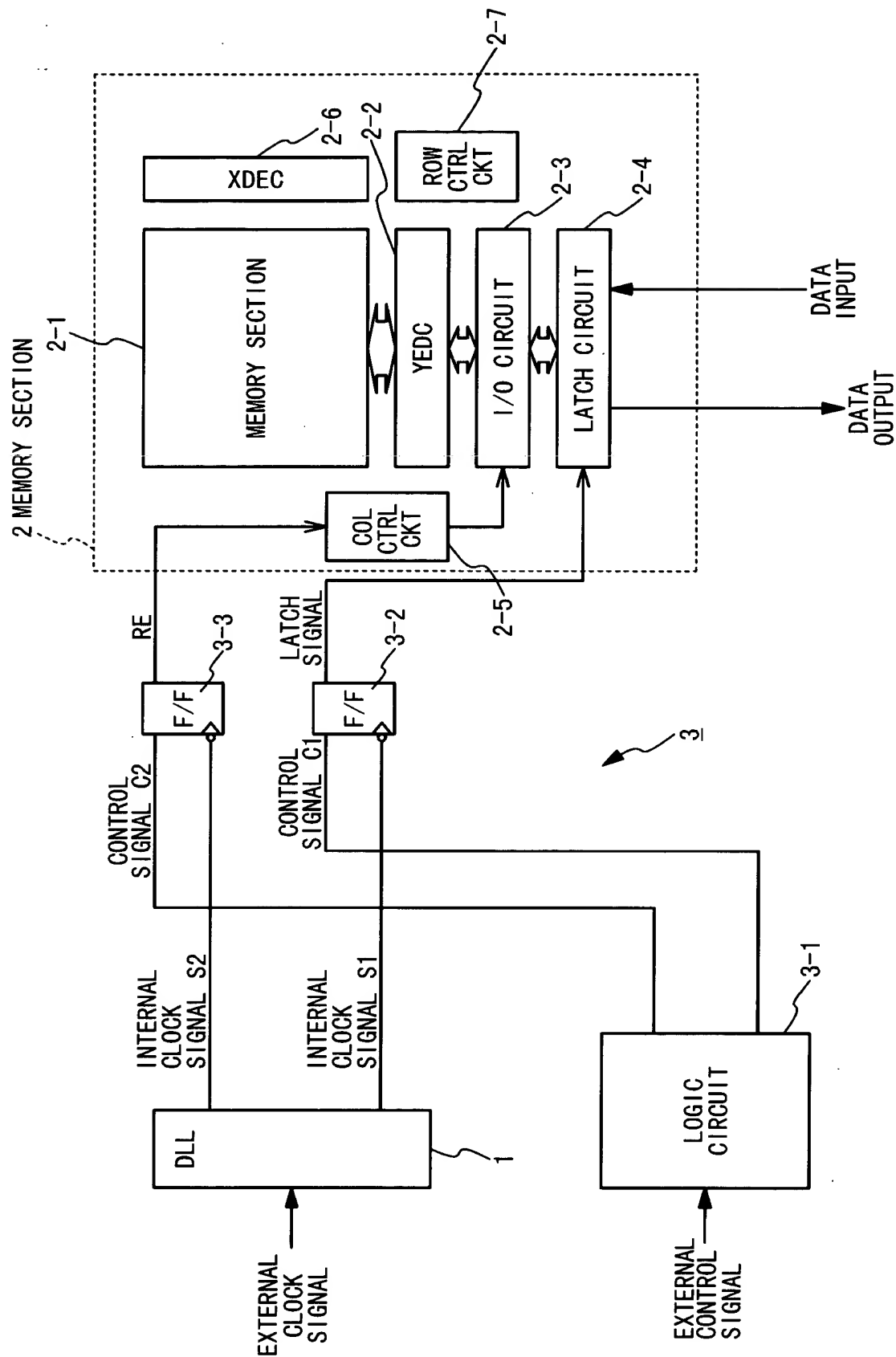


Fig. 12A

INTERNAL CLOCK SIGNAL S1



Fig. 12B

INTERNAL CLOCK SIGNAL S2

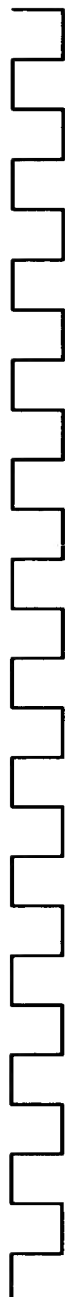


Fig. 12C

EXTERNAL CONTROL SIGNAL



Fig. 12D

CONTROL SIGNAL C2

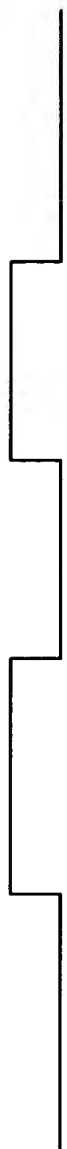


Fig. 12E

SIGNAL RE



Fig. 12F

DATA



Fig. 12G

CONTROL SIGNAL C1

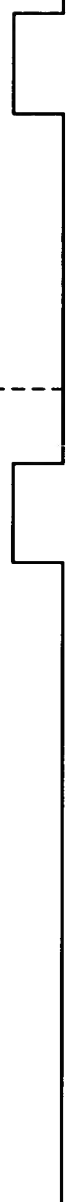


Fig. 12H

LATCH SIGNAL



LATCH DATA A

